

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re Application of:**

Warren M. Farnworth

**Serial No.:** Not Yet Assigned

**Filed:** January 9, 2002

**For:** STEREOLITHOGRAPHIC METHODS  
FOR FABRICATING HERMETIC  
SEMICONDUCTOR DEVICE PACKAGES  
AND SEMICONDUCTOR DEVICES  
INCLUDING  
STEREOLITHOGRAPHICALLY  
FABRICATED HERMETIC PACKAGES

**Examiner:** Unknown

**Group Art Unit:** Unknown

**Attorney Docket No.:** 4212.1US (99-0306.1)

*#2 Cited Art*  
*IDS*  
*Chick*  
*4-15-02*

J1040 U.S. PRO  
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01/09/02

**NOTICE OF EXPRESS MAILING**

Express Mail Mailing Label Number: EL 740549109 US

Date of Deposit with USPS: January 9, 2002

Person making Deposit: Orelana Howell

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
Washington, D.C. 20231

Sir:

The present application is a divisional of application Serial No. 09/640,801, filed August 17, 2000, pending.

Pursuant to M.P.E.P. 2001.06(b), the Examiner is respectfully requested to consider the information of record in the prior application, and to confirm in the first Office Action on the

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merits that such art has in fact been reviewed. A PTO-1449 form listing all of the information of record in the prior application is enclosed herewith.

This Information Disclosure Statement is filed within three (3) months of the filing date of the above-identified application, and no certification pursuant to 37 C.F.R. § 1.97(c) or a fee pursuant to 37 C.F.R. 1.17(p) is required.

Respectfully submitted,



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Date: January 9, 2002

BGP/ps:djp

Enclosure: Form PTO-1449